

RECEIVED
CENTRAL FAX CENTER

PATENT

JAN 09 2007

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Akhil Garlapati et al.

Title: VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA
EFFECT OF A CMOS BIPOLAR TRANSISTOR

Application No.: 10/813,837

Filed:

March 31, 2004

Examiner: Rajnikant B. Patel

Group Art Unit:

2838

Atty. Docket No.: 026-0044

Confirmation No.:

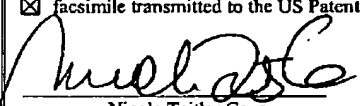
6047

January 9, 2006

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF
PATENT APPEALS AND INTERFERENCES**

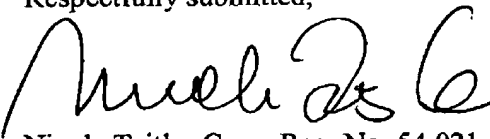
Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the last decision of the Examiner.

The fee for this Notice of Appeal under 37 C.F.R. § 41.20(b)(1) is submitted as set forth in the accompanying transmittal letter.

CERTIFICATE OF MAILING OR TRANSMISSION	
I hereby certify that, on the date shown below, this correspondence is being	
<input type="checkbox"/>	deposited with the US Postal Service with sufficient postage as first class mail and addressed as shown above.
<input checked="" type="checkbox"/>	facsimile transmitted to the US Patent and Trademark Office.
	<u>1/9/07</u>
Nicole Teitler Cave	Date

EXPRESS MAIL LABEL: N/A

Respectfully submitted,


Nicole Teitler Cave, Reg. No. 54,021
Attorney for Applicant(s)
(512) 338-6315 (direct)
(512) 338-6300 (main)
(512) 338-6301 (fax)

01/10/2007 TL0111 00000021 10013837

01 FC:1401

500.00 OP

- 1 -

notice of appeal

Application No.: 10/813,837